



rogeraylagas97@gmail.com

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# Roger Aylagas Torres

## Experience

07/2018 -07/2019 Vision Systems Engineer Intern at FICOSA

Design and Development of a rear-view mono camera extrinsic calibration algorithm.

## Projects

09/2018 -09/2019 Driverless UPC (Formula Student Team)

Vision Systems Engineer

Development and integration of AI system for track limit detection.

## Education

2019-Present Master in Advanced Telecommunication Technologies – Data Science and AI

Universitat Politècnica de Catalunya

2015-2019 Telecommunication Systems Engineering minor in Audiovisual Systems.

Universitat Politècnica de Catalunya

## Skills

Programming Languages: C++, Python

Miscellaneous: Matlab

Git

## Languages

English C1

Catalan Maternal Language

Spanish Maternal Language

## Other Experience

05/2018 - 12/2018 Leader of Project Planification

Telecommunication`s Student Delegation